

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Madan et al.

Docket No.: TI-34844

Serial No.: TBD

Examiner: TBD

Filed: Herewith

Art Unit: TBD

For: CIRCUIT AND METHOD FOR REDUCING ACCESS TRANSISTOR  
GATE OXIDE STRESS

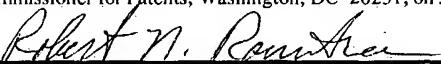
**INFORMATION DISCLOSURE STATEMENT**

July 2, 2003

Assistant Commissioner for Patents  
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

I hereby certify that this correspondence is being deposited with the  
United States Postal Service in an envelope addressed to: Assistant  
Commissioner for Patents, Washington, DC 20231, on July 2, 2003.

  
\_\_\_\_\_  
Robert N. Rountree, Reg. No. 39,347

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the listed references are enclosed.

This information disclosure statement is being mailed before receipt of a first office action on the merits in accordance with 37 C.F.R. 1.97(b)(3).

Respectfully submitted,



Robert N. Rountree  
Attorney for Applicants  
Reg. No. 39,347

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Approved for use through 10/31/2002. OMB 0651-0031

Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for Form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				<b>Complete If Known</b>	
Sheet	1	of	2	Application Number	TBD
				Filing Date	July 2, 2003
				First Named Inventor	Madan
				Group Art Unit	TBD
				Examiner Name	TBD
				Attorney Docket No.	TI-34844

<b>U.S. PATENT DOCUMENTS</b>					
Exam. Initials	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)
		Number	Kind Code <sup>2</sup> (if known)		
	AA				
	AB				
	AC				
	AD				
	AE				
	AF				
	AG				
	AH				
	AI				
	AJ				
	AK				
	AL				
	AM				
	AN				
	AO				

<b>FOREIGN PATENT DOCUMENTS</b>					
Exam. Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)
		Office <sup>3</sup>	Number <sup>4</sup>		
	BA				
	BB				
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	BE				
	BF				
	BG				
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	BJ				
	BK				
	BL				

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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				Group Art Unit	<i>TBD</i>
				Examiner Name	<i>TBD</i>
				Attorney Docket No.	<i>TI-34844</i>

<b>OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS</b>				
Exam. Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
	CA	<i>ALI SHEIKHOLESLAMI et al., A Survey of Circuit Innovations In Ferroelectric Random-Access Memories, Proc. of the IEEE, Vol. 88, No. 5, 667 (May 2000).</i>		
	CB	<i>ALI SHEIKHOLESLAMI, Ferroelectric Memory Design (FeRAM 101), ISSCC 2002 Tutorial (February 3, 2002).</i>		
	CC			
	CD			
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